

10 EE 32P ELECTRICAL & ELECTRONICS ENGG.LAB
III B.Tech II Semester

(with effect from the academic year 2012-2013)

Credits: 4

Periods/week: 3

Sessional Marks: 40

University Exam :3 Hrs

End Examination Marks: 60

*(Any **Eight** or more of the following experiments will be given)*

LIST OF EXPERIMENTS

ELECTRICAL

1. Verification of KVL and KCL
2. Load test on D.C Shunt Motor
3. Load test on 3 Phase Induction Motor
4. Sumburne's test
5. OC and SC Test on Single Phase Transformer

ELECTRONICS

1. V I Characteristics of PN Junction Diode and Zener Diode
2. Out put characteristics of npn transistor
3. Single Phase Bridge Rectifier
4. OP-amp applications
5. V I Characteristics of FET